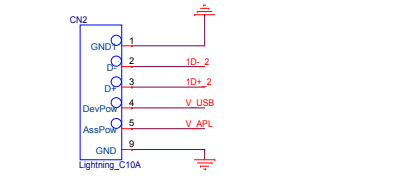
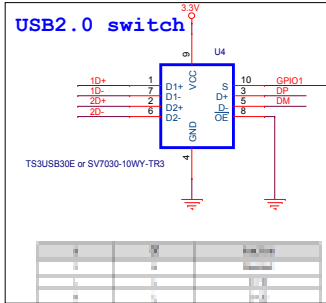
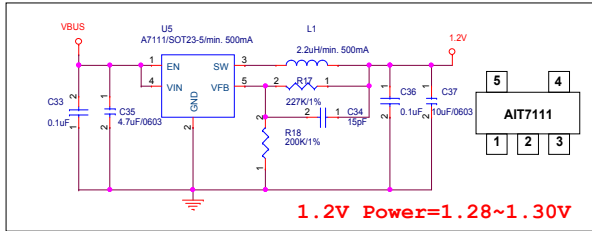
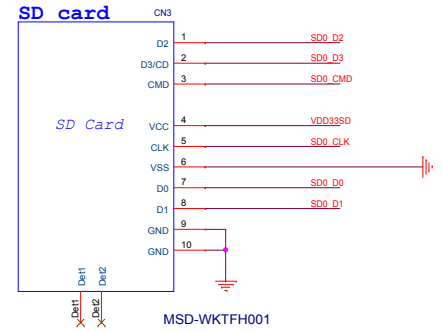
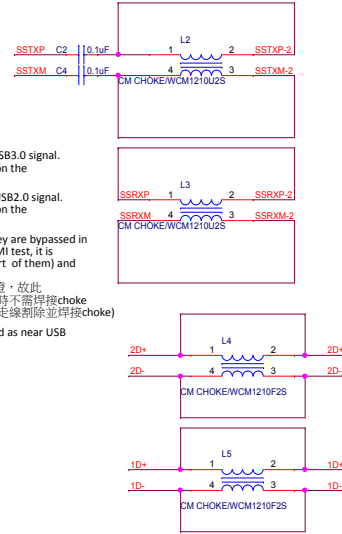


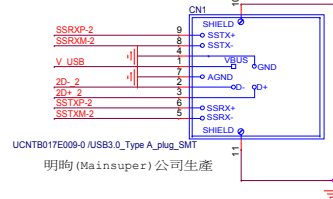
Note:

- Optional L2, L3 for reducing EMI of USB3.0 signal. The impedance of the choke depends on the impedance of USB3.0 trace on PCB
- Optional L4, L5 for reducing EMI of USB2.0 signal. The impedance of the choke depends on the impedance of USB2.0 trace on PCB
- L2~L5 do not be applied because they are bypassed in the schematic. If PCB can't pass for EMI test, it is needed to cut the bypass trace(for short of them) and apply them for EMI reduction. (因不一定需要choke才能通過EMI認證，故此電路已先將CM choke旁路，EMI初測時不需焊接choke，若EMI無法通過測試，再將旁路的走線割除並焊接choke)
- C2, C4, L2, L3, L4, L5 should be placed as near USB connectors as possible



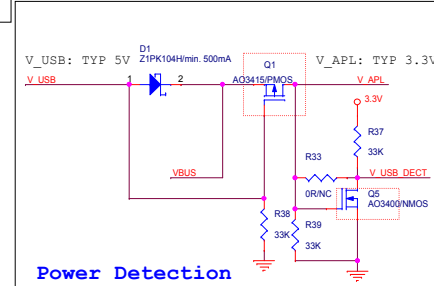
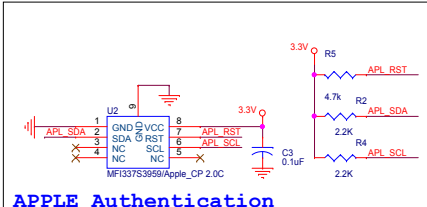
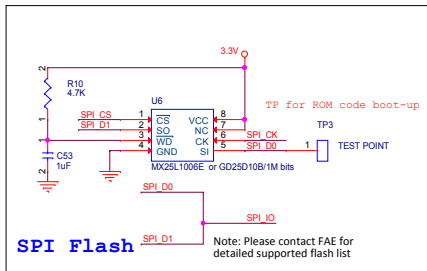
Note

- Trace width between pin1 of CN1 and pin4 of CN4 should be based on the charging current of Apple device (CN1的pin1(供應電源腳，Net V_USB) 及CN2的pin4(充電腳，Net V_USB) 相連線路的線寬都需根據Apple裝置的充電電流量來設計)
- If the device does not need the charging function for Apple device, pin4 of CN2 can be floated. (若不需Apple裝置的充電功能，CN2的pin4可floating)



Note:

- For using USB3.0 standard type A plug, the SSTX(or SSRX) of plug should be connected to SSRX(or SSTX) of PL2733.(Refer to the figure 6-4 in page 144 of USB 3.0 spec.) (使用USB3.0 type A公座 連接器時，連接器的TX需接到P2733的RX，連接器的RX需接到P2733的TX，詳細可參考USB3.0規格書144頁圖6-4的說明)
- To prevent from the cross routing of SSTX+ and SSTX- (or SSRX+ and SSRX-) between USB connector and PL2733 while doing PCB layout, SSTX+ and SSTX- (or SSRX+ and SSRX-) of PL2733 can be swapped and then connected to a USB connector. (PCB layout時，為了避免SSTX+及SSTX- (或SSRX+及SSRX-) 出現交叉佈線，PL2733的這兩組信號的同一組正及負信號可對調連接到USB連接器)



Note

- If device has the charging function for Apple device or the USB and lightning connectors can be attached to PC and Apple device at the same time, Q1 should be applied to isolate the two types of powered voltage to prevent from the damage of Apple device. (若此裝置具有充電功能或裝置的機構設計允許使用者將USB及lightning連接器同時插入PC及Apple裝置，因PC及Apple裝置兩者供應電壓不同，為了避免造成Apple裝置損壞，需使用Q1將兩端的電壓隔離)

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