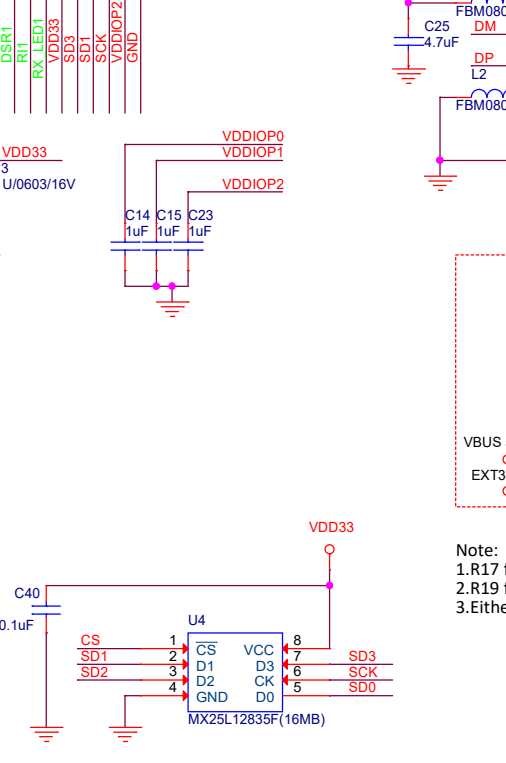
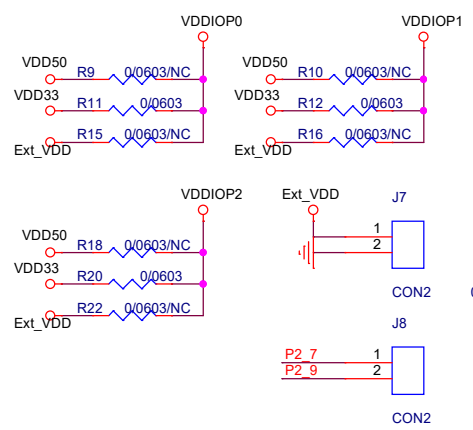
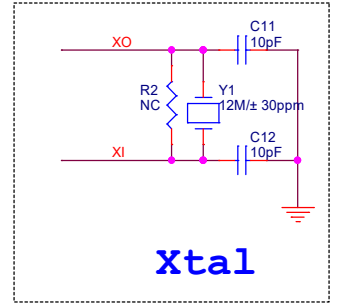
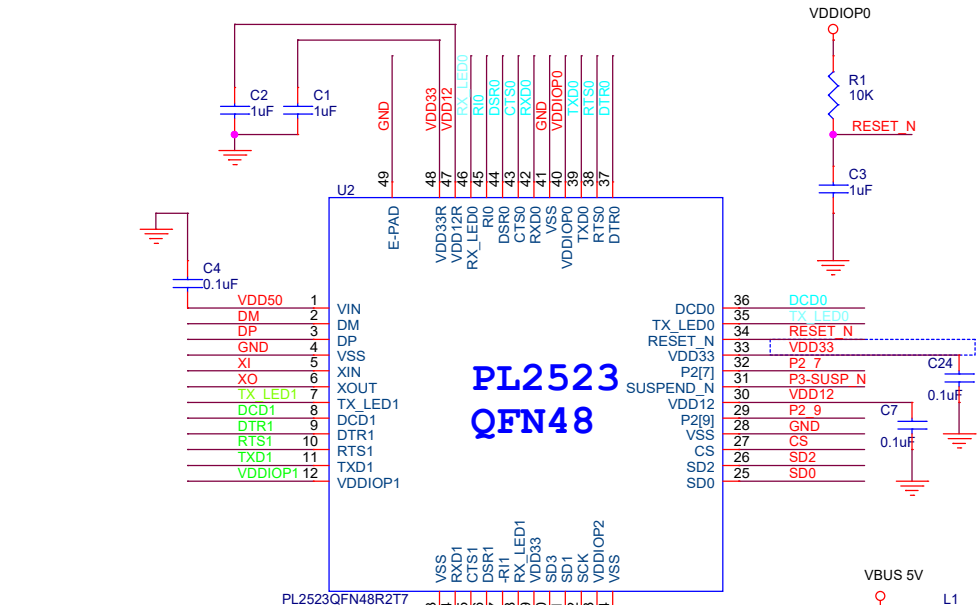
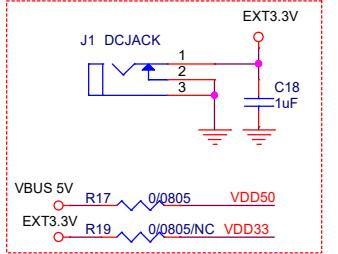


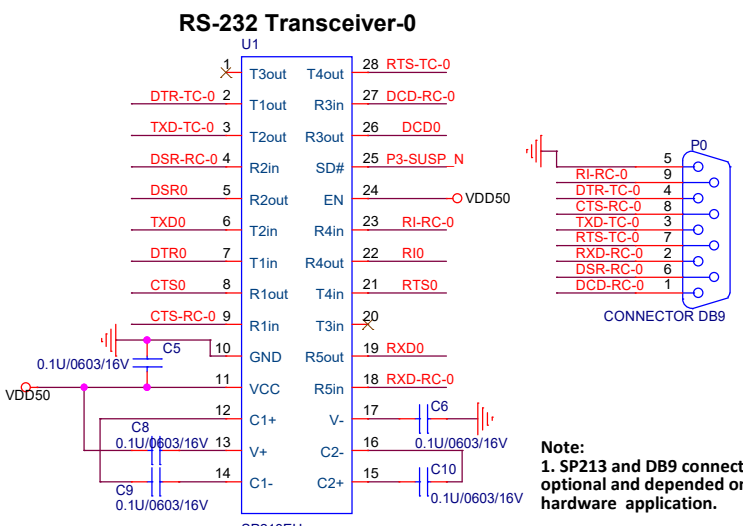
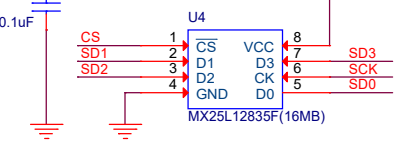


Note:
1. The optional SPI flash(Max. --16MB) is used for enabling a mass storage device to store the application software and storing the device info.(PID/VID, SN...).

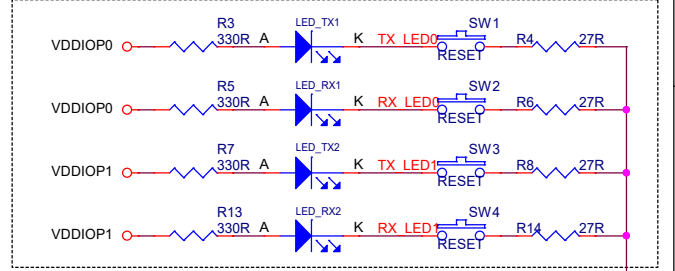
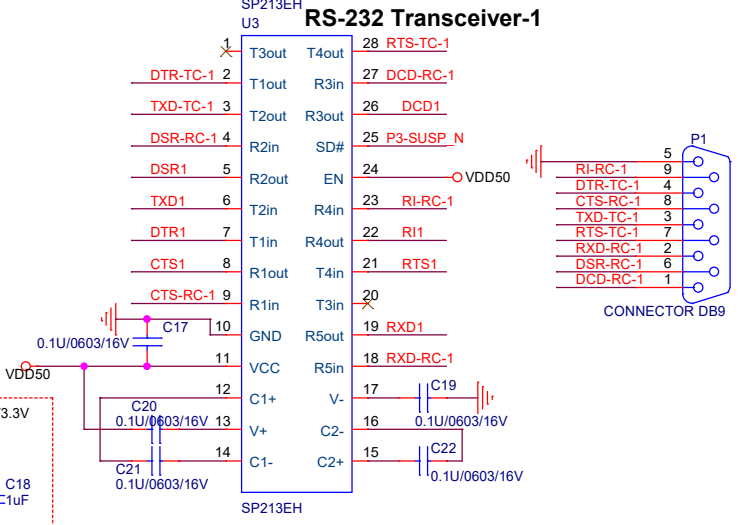
2. Please contact Prolific's FAE to get the latest supported flash list.



Note:
1.R17 for USB bus-powered
2.R19 for 3.3V self-powered
3.Either R17 or R19 is applied



Note:
1. SP213 and DB9 connectors are optional and depended on your hardware application.



Optional for TX/RX accessing indicator and GPIO application

PROLIFIC		
Title		
PL2523_QFN48 reference schematic		
Size	Document Number	Rev
Custom		1.4
Date:	Monday, October 14, 2024	Sheet 1 of 1